

FIG. 1

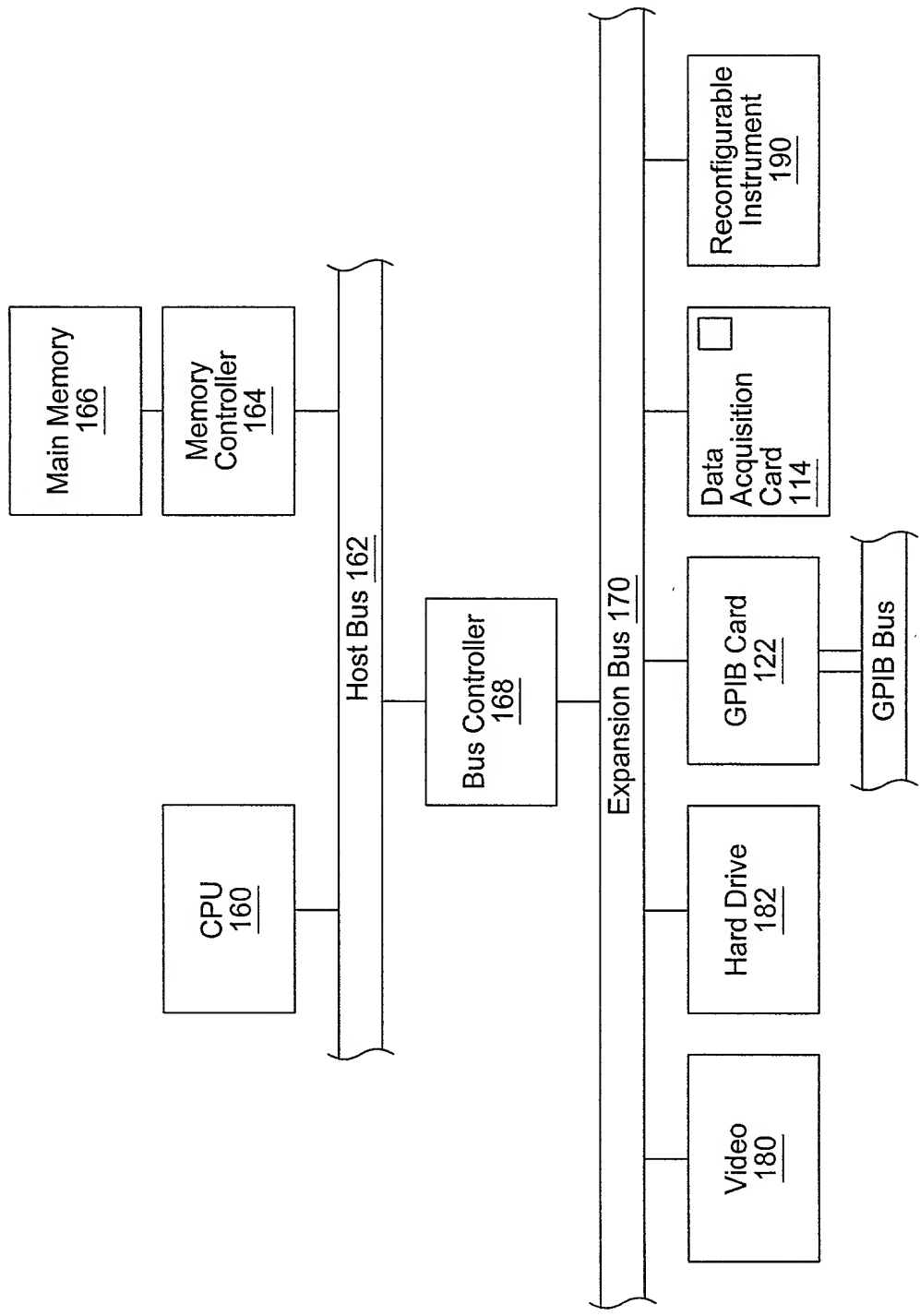


FIG. 2

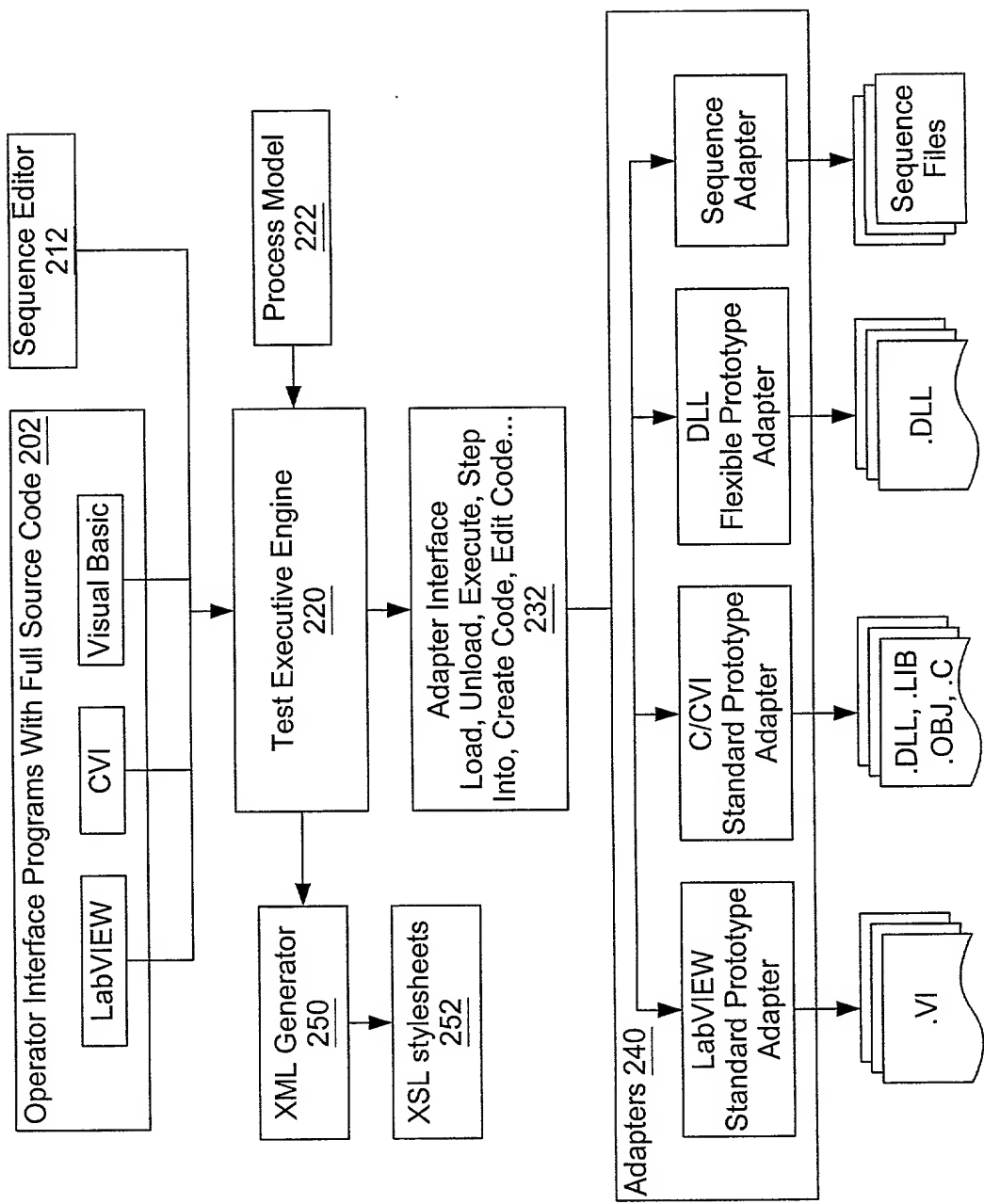


FIG. 3

C:\TestStand\Examples\XMLReports\computer.seq				- 0 X	
Main		View: MainSequence			
Setup	Cleanup	Parameters	Locals		
Step	Description	Execution Flow	Comment		
Power On	Numeric Limit Test, 12 <= x <= 15, volt, Call P...				
CPU	Call MainSequence in cpu.seq	Precondition			
ROM	Pass/Fail Test, Call ROMTest (computer.dll)	Precondition			
RAM	Pass/Fail Test, Call RAMTest (computer.dll)	Precondition			
Video	Numeric Limit Test, 70 <= x <= 75, hertz, Call ...	Precondition			
Keyboard	Pass/Fail Test, Call KeyboardTest (computer.dll)	Precondition			
ROM Diagnostics	Action, Call ROMDiagnostics (computer.dll)	Precondition			
RAM Diagnostics	Action, Call RAMDiagnostics (computer.dll)	Precondition			
Video Diagnostics	Action, Call VideoDiagnostics (computer.dll)	Precondition			
Keyboard Diagnostics	Action, Call KeyboardDiagnostics (computer.dll)	Precondition			
END					
				No Steps Selected	Number of Steps: 10

FIG. 4

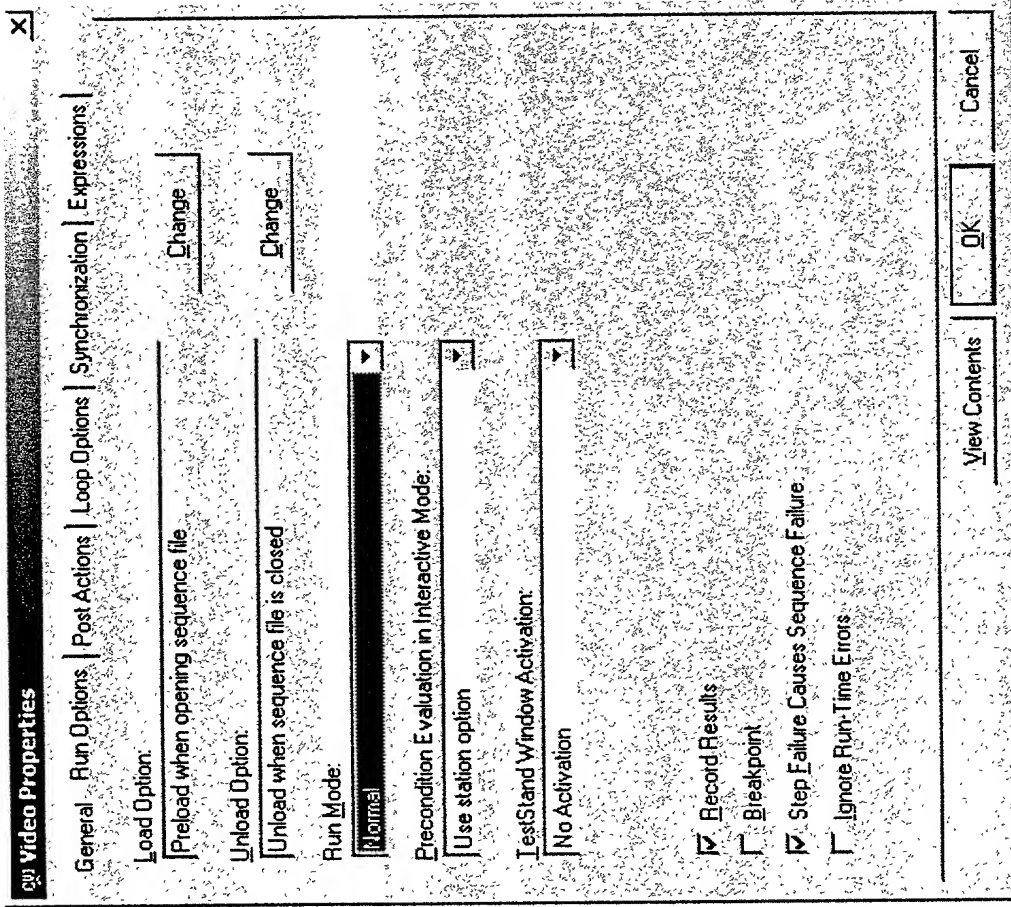


FIG. 5

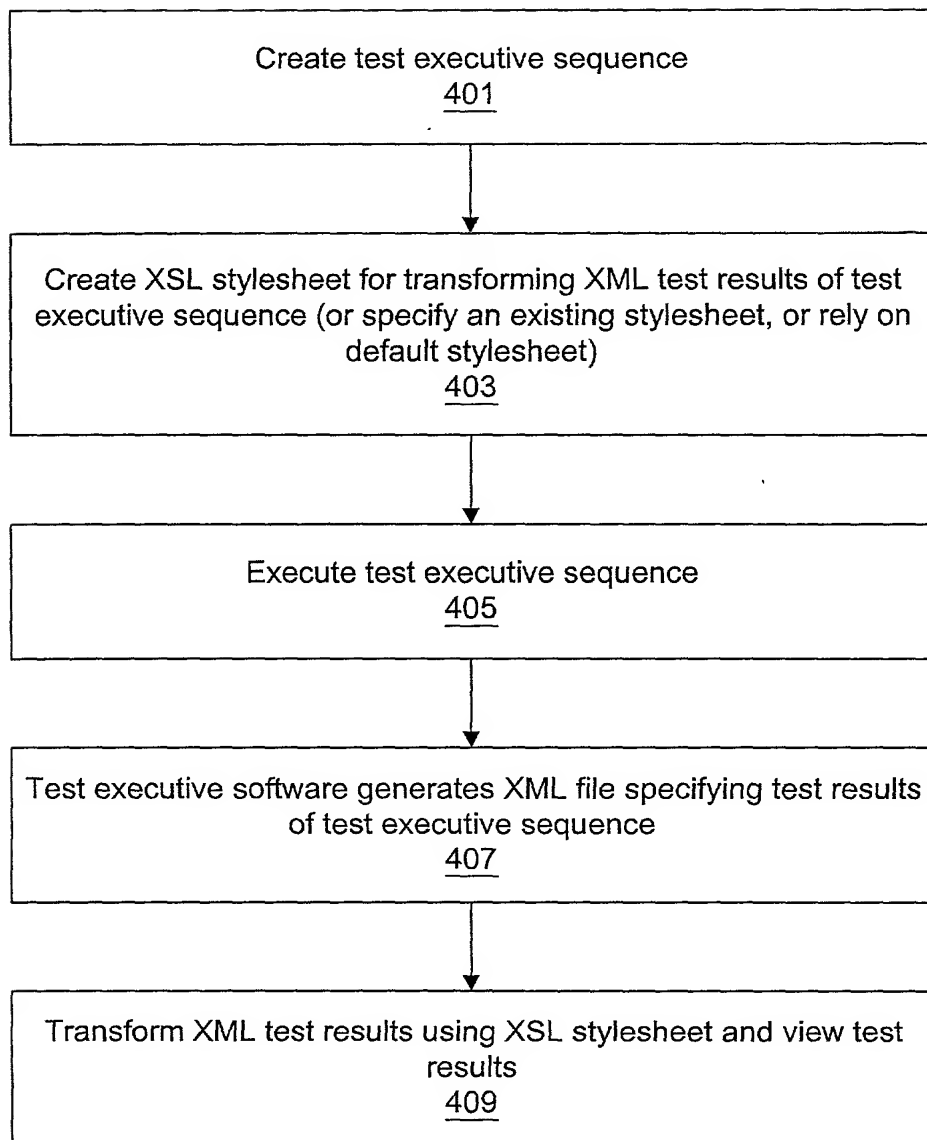


FIG. 6

UUT Report

- Station ID: TCASTRO
- Serial Number: NONE
- Socket Index: 1
- Date: Friday, February 09, 2001
- Time: 3:21:59 PM
- Operator: administrator
- Number of Results: 18
- UUT Result: Failed
- Failure Chain:

RAM	MainSequence	computer.seq

10056975.012502

FIG. 7A

Begin Sequence: MainSequence
 (C:\TestStand\Examples\XMLReports\computer.seq)

Check If Ram From Main Sequence	
Status:	Skipped
Explanation	
Status:	Skipped
Pre-test	
Status:	Done
Power On	
Status:	OK
Numeric:	15
Units:	volt
Limits:	
Low:	12
High:	15
Comp:	GELE <=>
CHD	
Status:	OK

10056975-013502

FIG. 7B

Begin Sequence: MainSequence
(C:\TestStand\Examples\XMLReports\cpu.seq)

Pre-test	
Status:	Done
Register	
Status:	
PassFail:	True
Instruction Set	
Status:	
PassFail:	True
Cache	
Status:	
PassFail:	True
FPU	
Status:	Failed
PassFail:	True

End Sequence: MainSequence

FIG. 7C

10056975-012502

ROM	
Status:	Passed
PassFail:	True
RAM	
Status:	Passed
PassFail:	False
Video	
Status:	Passed
Numeric:	75
Units:	hertz
Limits:	
Low:	70
High:	75
Comp:	GELE (>=<=)
Keyboard	
Status:	Passed
PassFail:	True
ROM Diagnostic	
Status:	Skipped
RAM Diagnostic	
Status:	Done
Video Diagnostic	
Status:	Skipped
Keyboard Diagnostics	
Status:	Skipped

End Sequence: MainSequence

End UUT Report

FIG. 7D

UUT Report

- Station ID: TCASTRO
 - Serial Number: NONE
 - Socket Index: 0
 - Date: Friday, February 09, 2001
 - Time: 3:21:59 PM
 - Operator: administrator
 - Number of Results: 18
 - UUT Result: Passed
-
-

10055975.012502

Begin Sequence: MainSequence
 (C:\TestStand\Examples\XMLReports\computer.seq)

Check if Run From Main Sequence	
Status:	Skipped
Explanation	
Status:	Skipped
Pre-test	
Status:	Done
Power-On	
Status:	Done
Numeric:	15
Units:	volt
Limits:	
Low:	12
High:	15
Comp:	GELE (>=<=)
GPI	
Status:	Done

10056975.012502

FIG. 7F

Begin Sequence: MainSequence
(C:\TestStand\Examples\XMLReports\cpu.seq)

Pre-Test	
Status:	Pass
Register	
Status:	Pass
PassFail:	True
Instruction Set	
Status:	Pass
PassFail:	True
Cache	
Status:	Pass
PassFail:	True
CPU	
Status:	Pass
PassFail:	True

End Sequence: MainSequence

10056975-042502

FIG. 7G

10056976-042502

ROM	
Status:	Pass
PassFail:	True
RAM	
Status:	Pass
PassFail:	True
Video	
Status:	Pass
Numeric:	75
Units:	hertz
Limits:	
Low:	70
High:	75
Comp:	GELE(>= <=)
Keyboard	
Status:	Pass
PassFail:	True
ROM Diagnostics	
Status:	Skipped
RAM Diagnostics	
Status:	Skipped
Video Diagnostics	
Status:	Skipped
Keyboard Diagnostics	
Status:	Skipped

End Sequence: MainSequence

End UUT Report

FIG. 7H

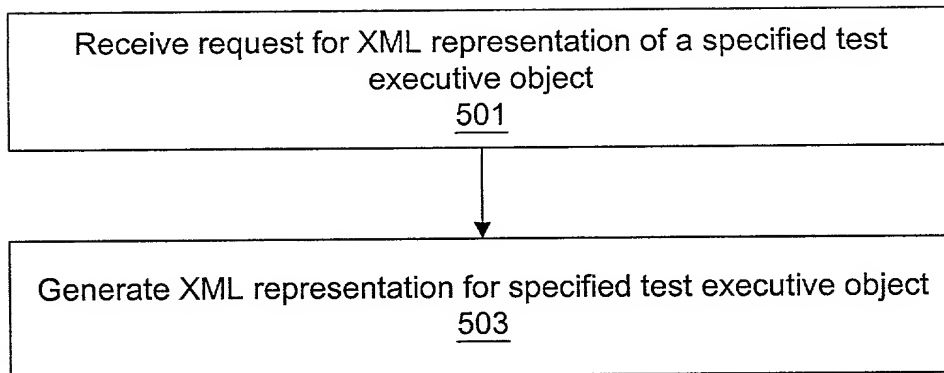


FIG. 8

10056975-012500

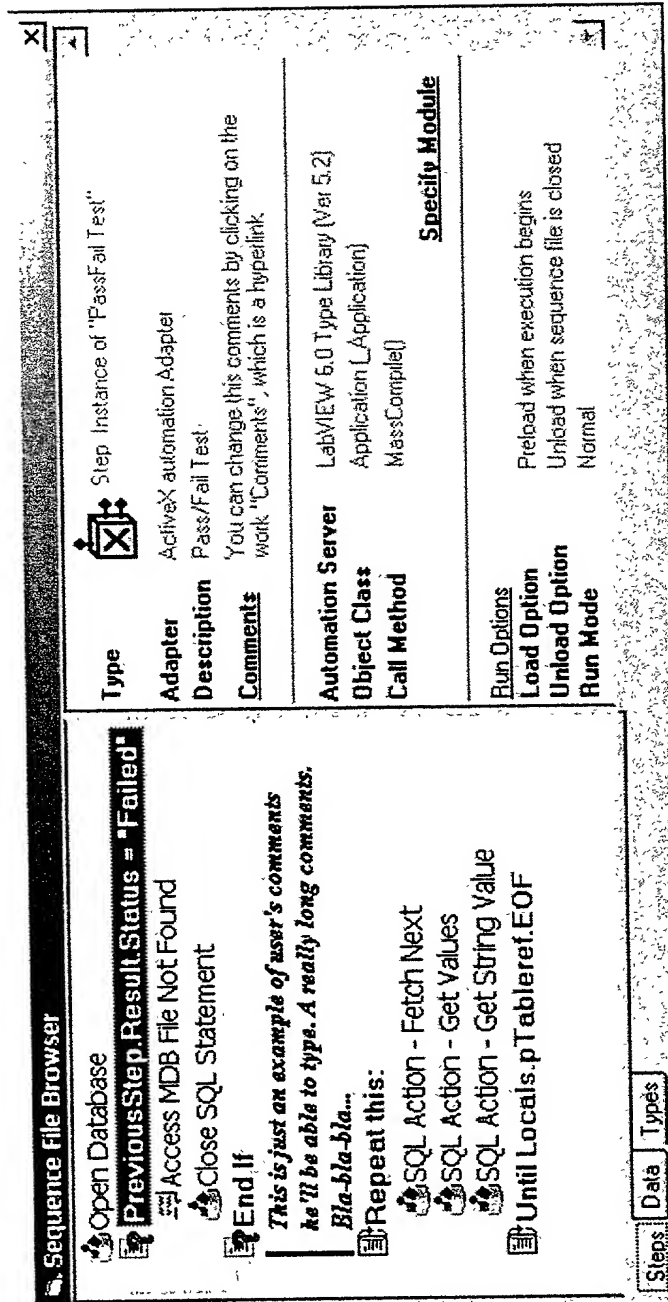


FIG. 9